

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	147960	"363"/	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/12 11:59
L2	33222	("363").CLAS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2008/02/12 11:59
L3	4050	l2 and (threshold level) with (current and (below under lower))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/12 13:12
L4	2903	l3 and transformer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/12 13:14
L5	2615	l4 and (prevent\$3 blank \$3 inhibit\$3 block\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/02/12 13:15
S1	32059	("363").CLAS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 11:56
S2	149	(363/21.15).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 11:56

S3	202	(363/21.16).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 11:58
S4	232	(363/21.18).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 11:58
S5	124	(363/56.1).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 11:58
S6	171	(363/56.11).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 12:01
S7	147	S2 and transformer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:01
S8	141	S7 and primary	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:01
S9	138	S8 and secondary	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:02
S10	133	S9 and current and control	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:03

S11	127	S10 and transistor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:03
S12	125	S11 and switch\$3	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:04
S13	98	S12 and pulse	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:05
S14	9	S13 and (memory)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:09
S15	24	S13 and (flip adj flop)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:10
S16	62	S13 and (logic or gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:44
S17	39	S13 and (integrated adj circuit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:40
S18	46	S13 and (current adj (limit\$5 or control\$3 or regulat\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:08

S19	25	S18 and (threshold)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 12:42
S20	8	S18 and S15 and S16 and S17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:09
S21	14	S16 and S15 and S17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:50
S22	200	S3 and transformer	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:51
S23	104	S3 and transformer and transistor and switch and mode and pulse	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:51
S24	3	S23 and (flip adj flop) and (logic adj gate) and (feedback or (feed adj back))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:52
S25	3	S23 and (flip adj flop) and (logic adj gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:53
S26	82	S23 and (feedback or (feed adj back))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:53

S27	31	S26 and integrated adj circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:55
S28	18	S26 and (flip and flop) and logic and gate	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 13:55
S29	42	S26 and threshold	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 15:59
S30	42	S26 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 14:29
S31	50	S23 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 16:00
S32	83	S4 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 16:29
S33	64	S32 and (logic or gate or flip or flop or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 16:30
S34	68	S5 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 16:52

S35	52	S34 and (logic or gate or flip or flop or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 16:53
S36	56	S6 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:20
S37	39	S36 and (logic or gate or flip or flop or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:21
S38	69	(363/21.05).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 17:19
S39	137	(363/21.07).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 17:19
S40	138	(363/21.08).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 17:19
S41	93	(363/21.09).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 17:20
S42	185	(363/21.1).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 17:20

S43	86	(363/21.11).OCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/01/23 17:20
S44	24	S38 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:28
S45	23	S44 and (logic or gate or flip or flop or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:28
S46	48	S39 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:34
S47	30	S46 and (logic or gate or flip or flop or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:34
S48	52	S40 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:44
S49	37	S48 and (logic or gate or flip or flop or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:44
S50	49	S41 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:50

S51	33	S50 and (logic or gate or flip or flop or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:50
S52	70	S42 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:50
S53	50	S52 and (logic or gate or flip or flop or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:50
S54	35	S43 and current adj (limit \$5 or regulat\$4 or maximum or minimum or signal)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:50
S55	34	S54 and (logic or gate or flip or flop or (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:51
S56	20	S53 and thresh\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/23 17:52
S57	7	"6385061"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 15:05
S58	2	"6324082"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 15:05



S59	14	"5703764"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 15:06
S60	4	"0525652"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 15:06
S61	4	"0525652"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 07:29
S62	964	band adj gap adj reference adj circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 11:31
S63	479	S62 and switch	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 11:31
S64	306	S63 and mode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 11:31
S65	283	S64 and power and supply	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 11:32
S66	197	S65 and feedback	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 11:32

S67	197	S66 and current	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 11:33
S68	86	S66 and current adj (limit \$3 or restrict\$3 or regulat \$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 15:53
S69	964	band adj gap adj reference adj circuit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 15:53
S70	479	S69 and switch	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 15:53
S71	306	S70 and mode	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 15:53
S72	283	S71 and power and supply	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 15:53
S73	197	S72 and feedback	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 15:53
S74	86	S73 and current adj (limit \$3 or restrict\$3 or regulat \$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 15:53

S75	59	S74 and memory and (flip adj flop)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 15:54
S76	19305	flip adj flop.ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 12:15
S77	19305	(flip adj flop).ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 12:16
S78	221	S77 and memory adj device	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 12:28
S79	12249	S77 and (flip adj flop). ab.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 12:31
S80	239	S79 and (s near r)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/12 12:31
S81	696	((363/21.15) or (363/21.16) or (363/21.18) or (363/56.1) or (363/56.11)).OCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/19 16:26
S82	786	((363/21.15) or (363/21.16) or (363/21.18) or (363/56.1) or (363/56.11) or (363/21.05) or (363/21.13)).OCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/19 16:28

S83	541	S82 and (logic flip adj flop gate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/19 16:29
S84	255	S83 and (threshold limit) with current	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/19 17:01
S85	339	S83 and (threshold limit bound\$4 edge end extent extreme border) with current	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/19 17:02
S86	2	("20020067629").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/20 14:52
S87	2	("0942518").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/20 14:52
S88	0	("0942518").PN.	EPO	OR	OFF	2007/09/20 14:52
S89	1	("1179884").PN.	EPO	OR	OFF	2007/09/20 14:54
S90	0	("0070730").PN.	EPO	OR	OFF	2007/09/20 14:58
S91	2	("5812383").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/09/20 14:59

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